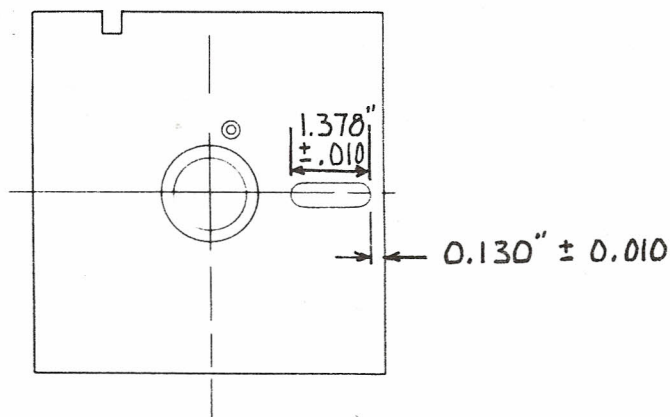


**INSTALLATION INSTRUCTIONS  
FOR PET II DISK CONTROLLER BOARD  
(400K or 800K)**

1. **\*\*\*\*\*POWER OFF\*\*\*\*\*** Remove the 2 right-hand screws holding the PET CPU board to the base (these screws are under the base, not thru the PC board). Open the hinged top of your PET and prop open with the stay.
  2. Mount the controller support brackets (Part no. 5000172-000) to the bottom of the controller board (Part no. 1000176) with the hardware provided (see figure 1).
  3. If you received an EPROM 1180 card, plug it into the connector on the top of the controller board. Install the EPROM card with the components facing the components (see figure 3). If you received a single masked ROM (Part no. 1000188-000), plug it into the most left-hand empty socket on the PET CPU board with pin 1 facing the front of the PET (see figure 2).
  4. Next, plug the ROM cable assembly (Part no. 1000182-000) into the third socket from the right on the PET CPU board with the flat cable toward the back right of the PET (see figure 2).
  5. Squeeze together the 1 plastic standoff at lower right and pop-off the CPU board (see figure 2). Place the controller board over the CPU with the brackets facing the left of the CPU and insert the ROM cable thru the rectangular hole in the controller board. Be careful not to knock down the 2 capacitors at the right rear of the PET CPU. Pick the PET CPU board up about 2 inches on the right-hand side and install the controller board on the 50 header pins on the inside row. The pins must be straight. Make sure the sockets are all the way down on the pins, but be careful.
- Re-install the CPU board in the base. Plug the ROM cable onto the 10 pin header adjacent to the hole in the controller board (J6 on figure 3).
6. Install the power cable (Part no. 1000180-000) onto the single row 10 pin header on the controller board (J1 on figure 3). Install the other 2 ends of the cable onto the two empty 7 pin headers on the CPU (see figure 2). Note the keying of the connectors so that they are installed correctly on the headers.
  7. Plug the 34 conductor flat cable (Part no. 1000272-000) on the 34-pin header on the controller board with the dark blue color coded edge of the flat cable facing the front of the PET (J7 on figure 3). The color coded side should be face down. The other end of the interconnect cable plugs into the drive with the color side facing the drive and the dark blue stripe facing up.
  8. Checkout procedure: Power on the PET. Power on the Disk Drive. (There is a separate switch for the Disk Driver on the back panel of the Drive). After Power On or Hardware Reset, enter SYS11\*4096 and push RETURN. DISKMON commands are now activated. Place the demo diskette in one of the drives with the label on the right and the head windows toward the rear. Consult DISKMON User's Guide for further details of the Disk Driver operation.

**WARNING**  
**CARE OF THE DISK DRIVE HEADS**

Always use diskettes that meet ANSI (American National Standards Institute) standards for the dimensions of the jacket. (See drawing).



Current major manufacturers' products meet this spec. However, certain older diskettes may have head windows smaller than this spec.

**UNDER NO CIRCUMSTANCES** use such diskettes in a double headed drive since they will damage the second head which is 4 tracks further inboard on the diskette.

## **INTERCHANGING DISKETTES BETWEEN SINGLE HEADED AND DOUBLE HEADED DRIVES**

If you wish to interchange a diskette between a single and a double head system, both must be double density format. Also, the double head drive can only read the first side of the single head drive since the second head of the double head drive is 4 tracks offset toward the center of the diskette. Hence, record data to be interchanged between such systems on the first side of the diskette.



**ATTENTION****NEW 16K AND 32K USERS AND  
8K PET USERS WITH NEW ROM'S FROM COMMODORE**

Last minute improvements to the operating system has produced a minor string anomaly in the \$ODISK and \$RDISK commands. This causes pointers to be set to non-existent string variables. To overcome this anomaly you must allocate each string variable used in the \$ODISK and \$RDISK at the beginning of the program, i.e.,: F\$="", R\$="", I\$="", etc., For example:

```
10F$=" ": R$=" ": I$=" "  
20$ODISK, D, T$, F$, I$  
30$RDISK, R$
```

As soon as correct ROM's and EPROM's are available we will ship them to you at no charge and will expect back from you your old ROM's and EPROM's.

8-24-79

SUPPORT BRACKET DIAGRAM  
BOTTOM OF CONTROLLER BOARD P/N 1000176

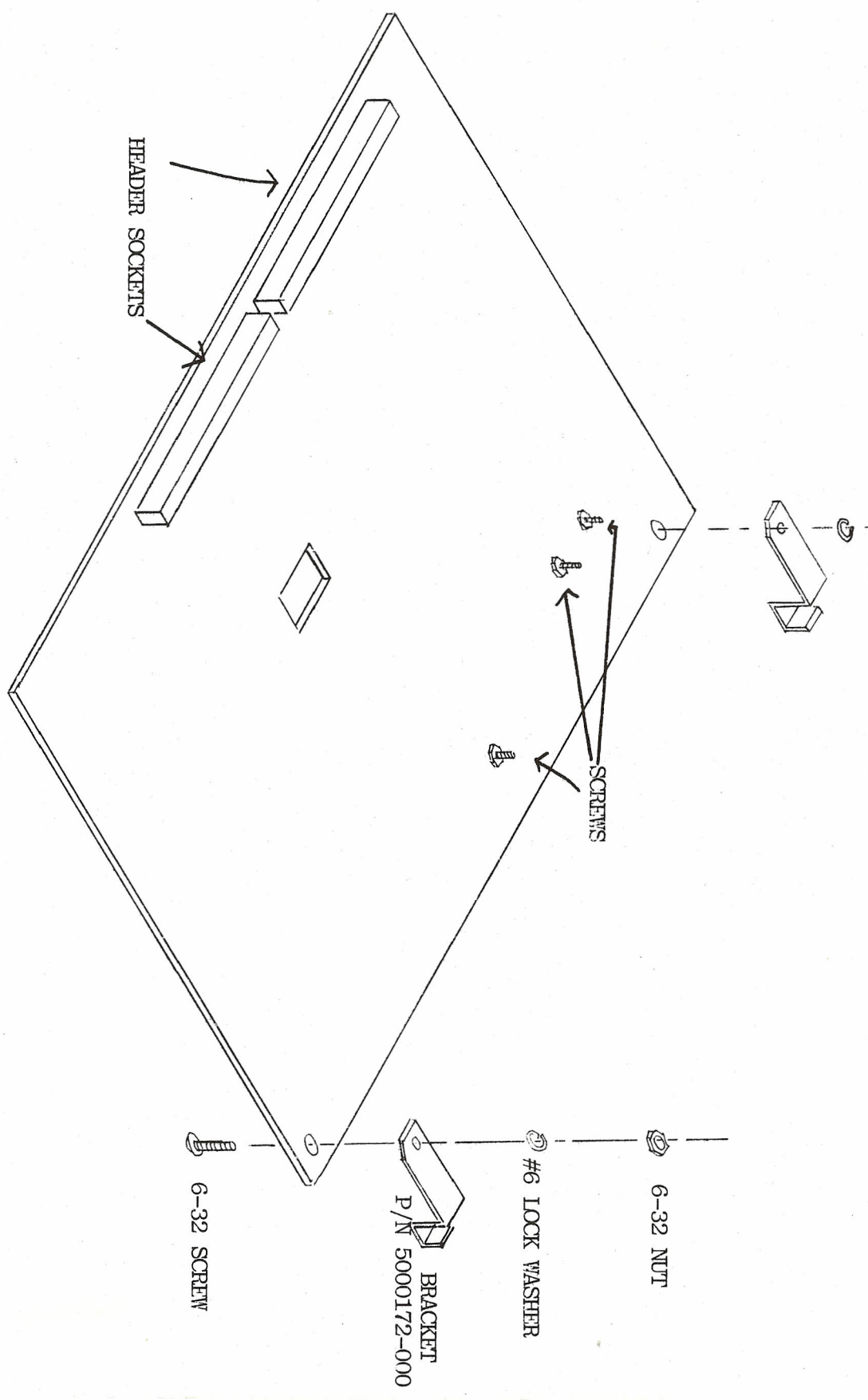


FIGURE 1.

# PET MAIN LOGIC BOARD

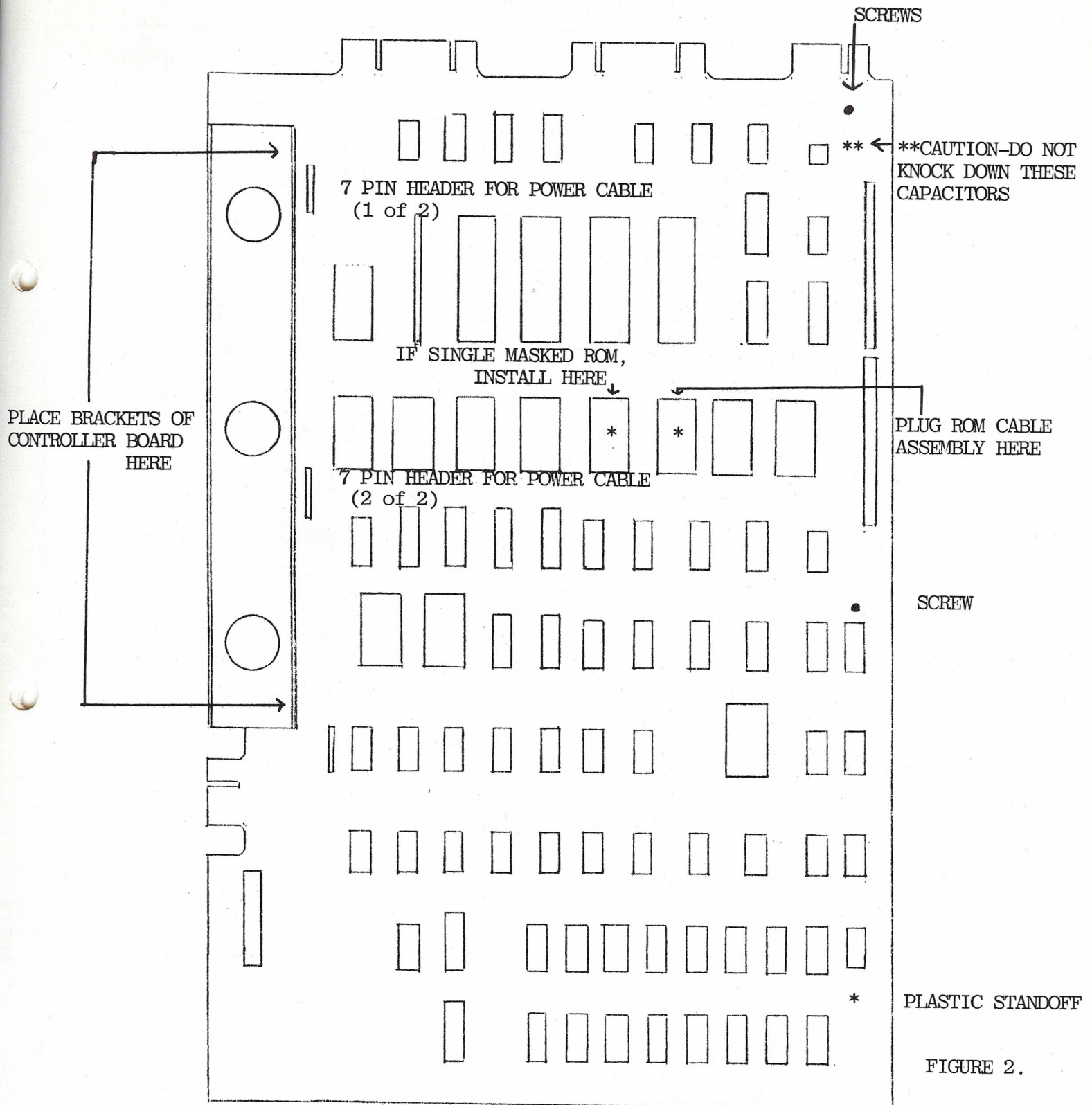
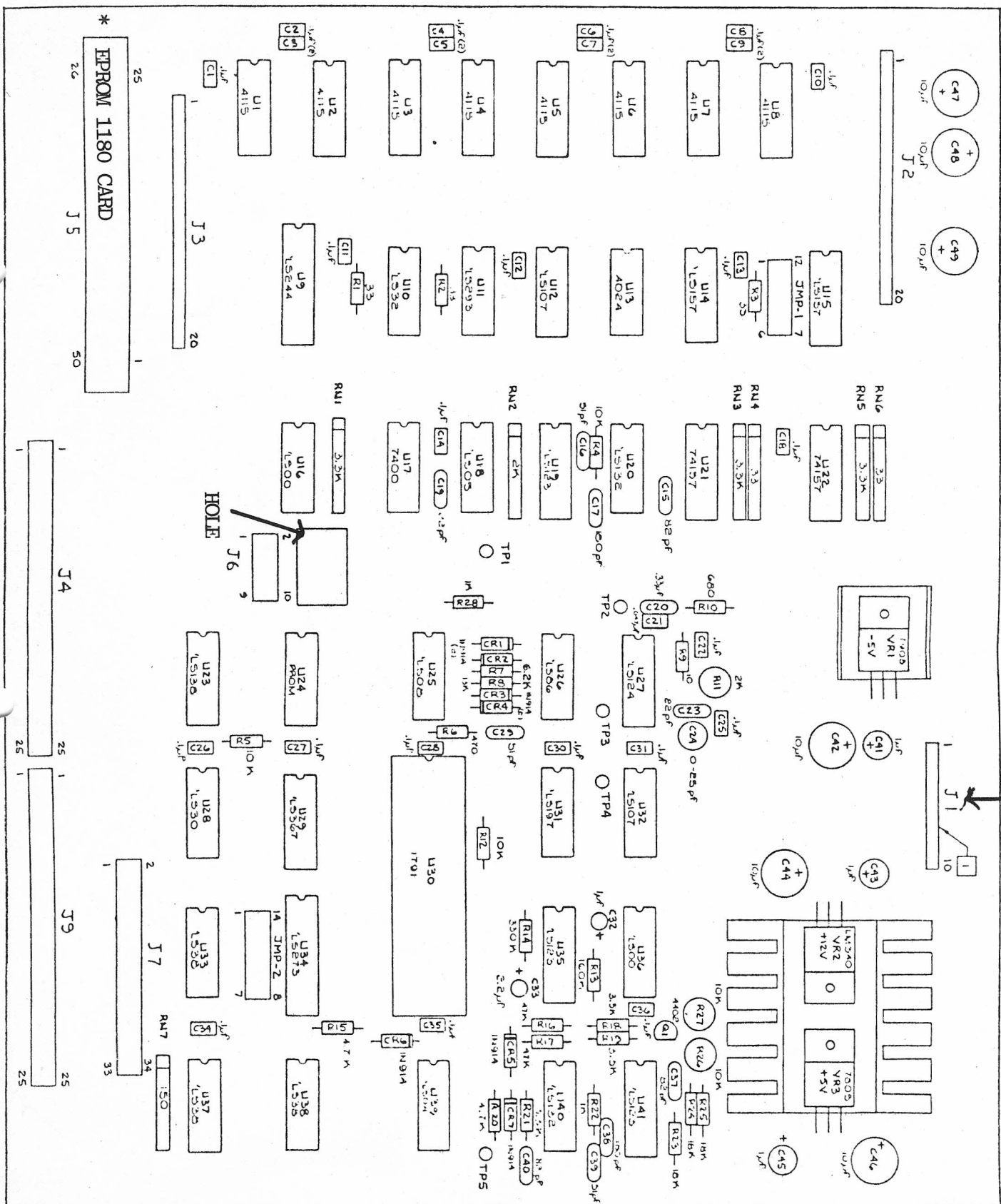


FIGURE 2.

# DISK CONTROLLER

10 PIN HEADER FOR POWER CABLE



\*INSTALL EPROM CARD WITH COMPONENTS FACING

FIGURE 3.